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Examiner Thinh T. Nguyen

FAX NO.:

571-273-8300

USPTO GPAU 2812

FROM:

Jeffrey S. Abel

Reg. No.: 36,079

RE U.S. App. No.: 10/828,680, filed April 21, 2004

Applicant(s): Andrew G. Haerle, et al.

Atty Dkt No.: 1075-E4283-DIV

Title:

METHOD FOR TREATING SEMICONDUCTOR PROCESSING

COMPONENTS AND COMPONENTS FORMED THEREBY

NO. OF PAGES (including Cover Sheet): 5

MESSAGE:

Attached please find:

Transmittal Form (I pg)

Response to Restriction Requirement (3 pgs)

5000 Plaza On The Lake Suite 265 AUSTIN, TEXAS 78746

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NO. 3465 P. 2

OCT 2 5 2005

Under the Paperwork Reduction Act of	995. po perso:	U.E Bee required to respond to a	6. Patent and		PTO/SB/21 (09-04) 36 through 07/31/2006. OMB 0651-0031 37 U.S. DEPARTMENT OF COMMERCE 5 it displays a valed OMB control number.	
		Application Number	10/828		STO GROBNES & VANG CIMB COMPO number.	
TRANSMITTAL FORM		Filing Date April 21, 2004				
		First Named Inventor		Andrew G. Haerle		
		Art Unit	2818			
(to be used for all correspondence after in	itial filing)	Examiner Name	Thinh I	. Nguyen		
Total Number of Pages in This Submission	Attorney Docket Number	1075-E	1075-E4283-DIV			
ENCLOSURES (Check all that apply)						
Fee Attached	I	Drawing(s) Licensing-related Papers		App	r Allowance Communication to TC eal Communication to Board opeals and Interferences	
After Final After Final Aftidavits/declaration(s) Extension of Time Request Express Abandonment Request Information Disclosure Statement Certified Copy of Priority Document(s) Reply to Missing Parts/		<u></u> -	Address	(App	eal Communication to TC eal Notice, Brief, Reply Brief) wietary Information us Letter or Enclosure(s) (please Identify w):	
Reply to Missing Parts under 37 CFR 1.52 or 1.53	3	FOMER NO.: 34		R AGENT		
TOLER, LARSON & ABEL, LLP						
Signature ///////						
Printed name Jeffrey S. Abel						
Date , 10/25/2005	10/25/2005 Reg. No. 36,079					
CERTIFICATE OF TRANSMISSION/MAILING						
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Typed or printed name Elise K. Doug	nerty ()	Q		Date	10/25/2005	

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PAGE 2/5 * RCVD AT 10/25/2005 3:26:58 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-6/37 * DNIS:2738300 * CSID:512 327 5452 * DURATION (mm-ss):01-36

NO. 3465

OCT 2 5 2005

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Andrew G. Haerle, et al.

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METHOD FOR TREATING SEMICONDUCTOR PROCESSING

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App. No.:

10/828,680

Filed:

April 21, 2004

Examiner:

Thinh T. Nguyen

Group Art Unit:

2818

Atty. Dkt. No.: 1075-E4283-DIV

MS AMENDMENT

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the restriction requirement mailed September 29, 2005, please amend the above-identified application as follows.